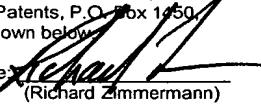


I hereby certify that this correspondence is being deposited with the U.S. Postal Service as Express Mail, Airbill No. EV 323777241 US, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date shown below.

Dated: July 10, 2003

Signature: 
(Richard Zimmermann)

Docket No.: 29926/39500
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Won-Ho Lee

Application No.: Not Yet Assigned

Group Art Unit: N/A

Filed: Concurrently Herewith

Examiner: Not Yet Assigned

For: **METHOD FOR FABRICATING IMAGE
SENSOR INCLUDING ISOLATION LAYER
HAVING TRENCH STRUCTURE**

INFORMATION DISCLOSURE STATEMENT (IDS)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 CFR 1.56, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached PTO/SB/08. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement accompanies the new patent application submitted herewith. A copy of each reference on PTO/SB/08 is attached. A concise explanation of relevance of the items listed on form PTO/SB/08 is not given.

While the information and references disclosed in this Information Disclosure Statement may be "material" pursuant to 37 CFR 1.56, it is not intended to constitute an admission that any patent, publication or other information referred to therein is "prior art" for this invention unless specifically designated as such.

In accordance with 37 CFR 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 CFR 1.56(a) exists. It is submitted that the Information Disclosure Statement is in compliance with 37 CFR 1.98 and the Examiner is respectfully requested to consider the listed references.

The Director is hereby authorized to charge any deficiency in the fees filed, asserted to be filed or which should have been filed herewith (or with any paper hereafter filed in this application by this firm) to our Deposit Account No. 13-2855. A duplicate copy of this paper is enclosed.

Dated: July 10, 2003

Respectfully submitted,

By Mark H. Hopkins

Mark H. Hopkins, Ph.D.

Registration No.: 44,775

MARSHALL, GERSTEIN & BORUN

233 S. Wacker Drive, Suite 6300

Sears Tower

Chicago, Illinois 60606-6357

(312) 474-6300

Attorney for Applicant

Substitute for form 1449/PTO				Complete if Known	
				Application Number	Not Yet Assigned
				Filing Date	Concurrently Herewith
				First Named Inventor	Won-Ho Lee
				Art Unit	N/A
				Examiner Name	Not Yet Assigned
Sheet	1	of	1	Attorney Docket Number	29926/39500

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			

FOREIGN PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)			

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NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No. ¹	Include name of the author, title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			²
	C1	Fujieda <i>et al.</i> , "Dependence of Si PH Junction Perimeter Leakage on the Channel-Stop Boron Dose and Interlayer Material," <i>IEEE Electron Device Letters</i> , Vol. 20, No. 8, 418-420 (August 1999).			
	C2	Morris <i>et al.</i> , "An Accurate and Efficient Model for Boron Implants through Thin Oxide Layers into Single-Crystal Silicon," <i>IEEE Transactions on Semiconductor Manufacturing</i> 8(4), 408-413 (November 1995).			
	C3	Son <i>et al.</i> , "Blanket Tilt Implanted Shallow Trench Isolation (BTI0STI) Process for Enhanced Dram Retention Time Characteristics," <i>IEEE</i> 122-124 (1999).			
	C4	Walther <i>et al.</i> , "Dopant Channeling as a Function of Implant Angle for Low Energy Applications," <i>IEEE</i> 126-129 (1999).			

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